Title of the course: Computer Architecture

Lecturer: Dr.Y. Kord Tamandani

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Assessments:

Mid-term Exam	**5 points**
Final Exam	**10 points**
Class Activity	**3 points**
Attendance	**2 points**

Subjects to be discussed:

TOPICS	SESSIONS
Digital Logic Circuits **Digital Components** **Data Representation**	1st and 2nd sessions
Register Transfer **Micro operations**	3rd and 4th sessions
Basic Computer Organization and Design	5th and 6th Sessions

Programming the Basic Computer **Microprogrammed Control**	**7th and 8th session**
Central Progressing Unit (CPU). **Pipeline and Vector Processing.**	**9th ,10th and 11th sessions**
Computer Arithmetic **Input-Output Organization.**	**10th ,11th and 12th sessions**
Memory Organization **Multiprocessors**	**13th ,14th and 15th sessions**
Review the lectures	**16th and 17th sessions**